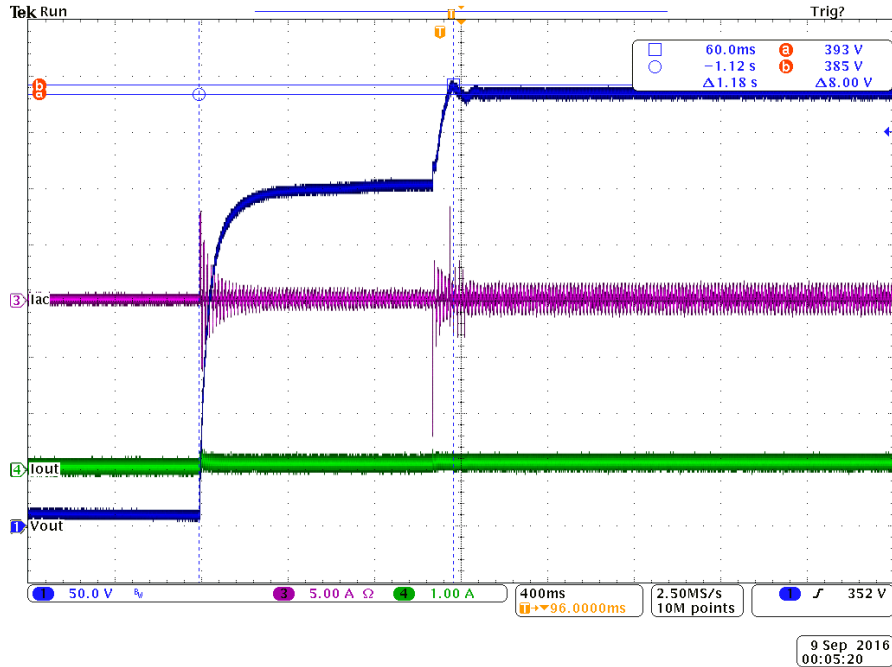


# **PMP20873 – 1kW Totem-Pole PFC EVM Test Report**

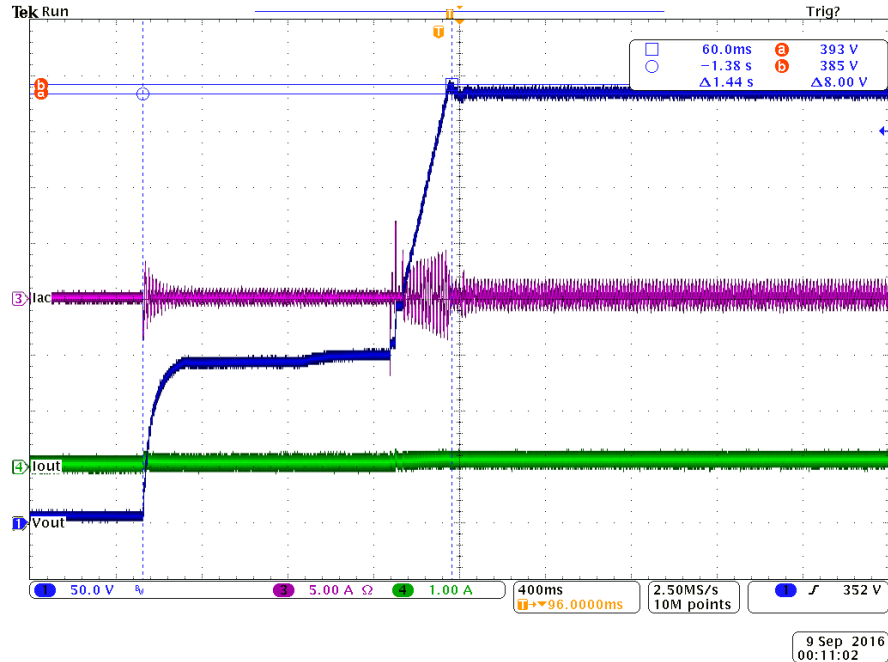
**GaN / Next  
Zhong Ye**

**Oct. 2016**

# Start up



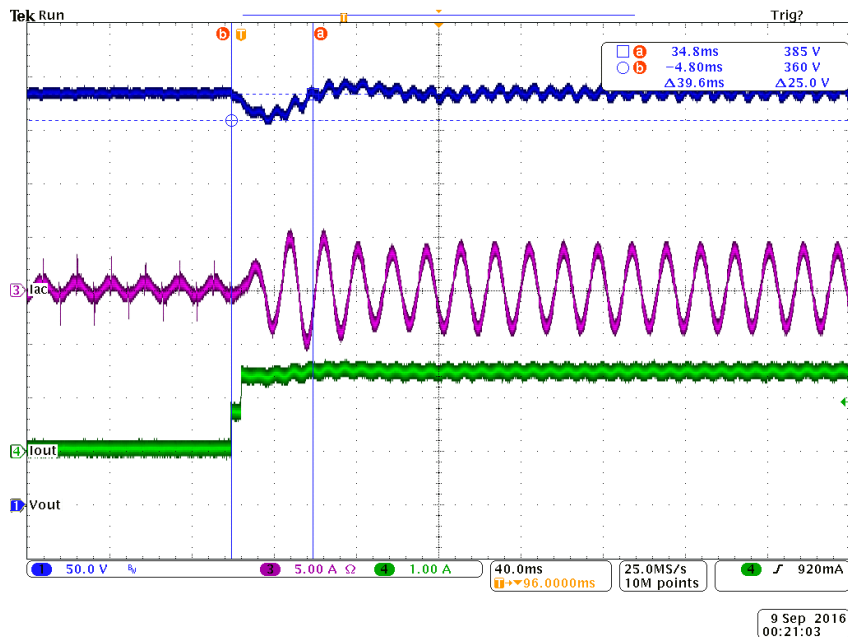
## High line startup



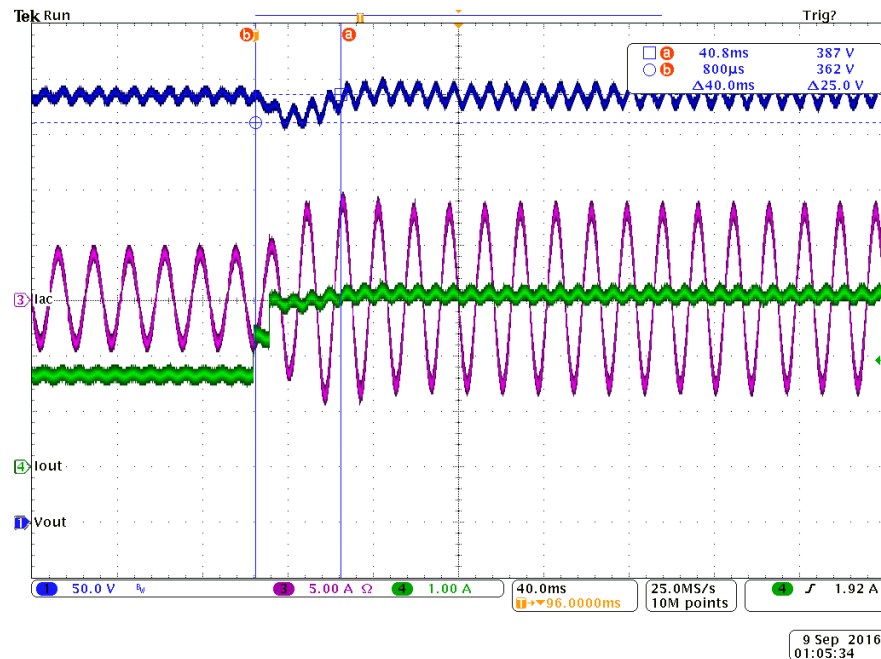
## Low line startup

Note: extra 0.9 second was inserted due to DCP010512 bias long startup time.  
The time can be eliminated when using bootstrap circuit.

# Step Load Response – High Line ( 230Vac)

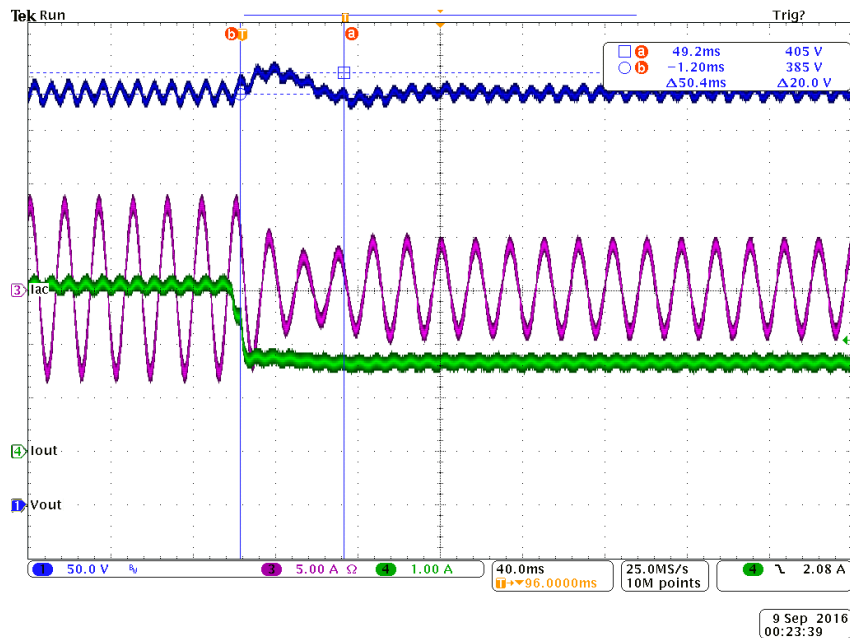


0 to 50% step load

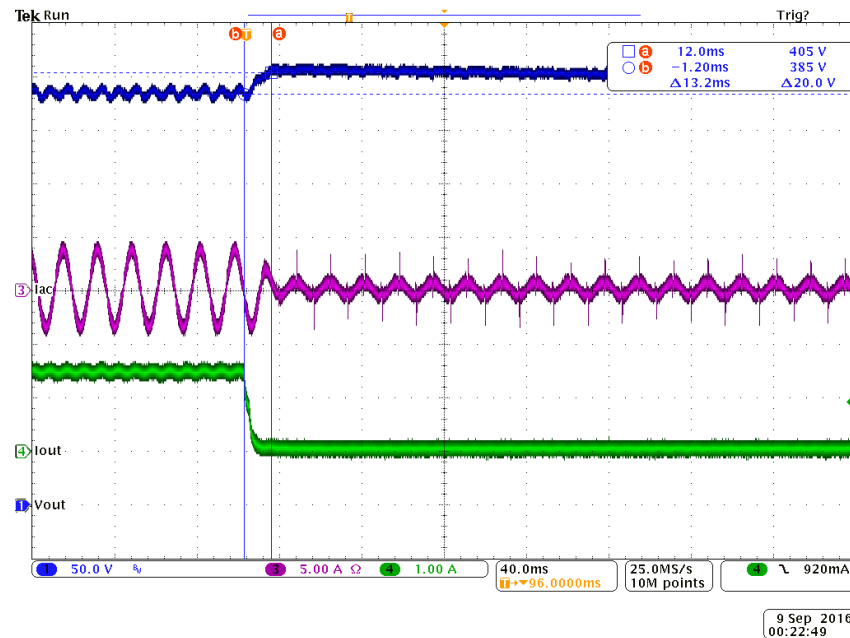


50 to 100% step load

# Step Load Response – High Line ( 230Vac)

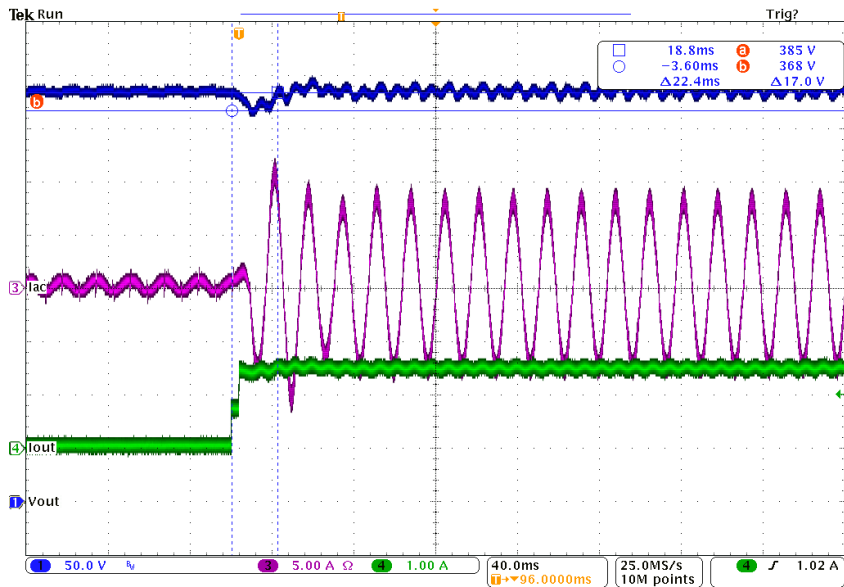


100% to 50% step load



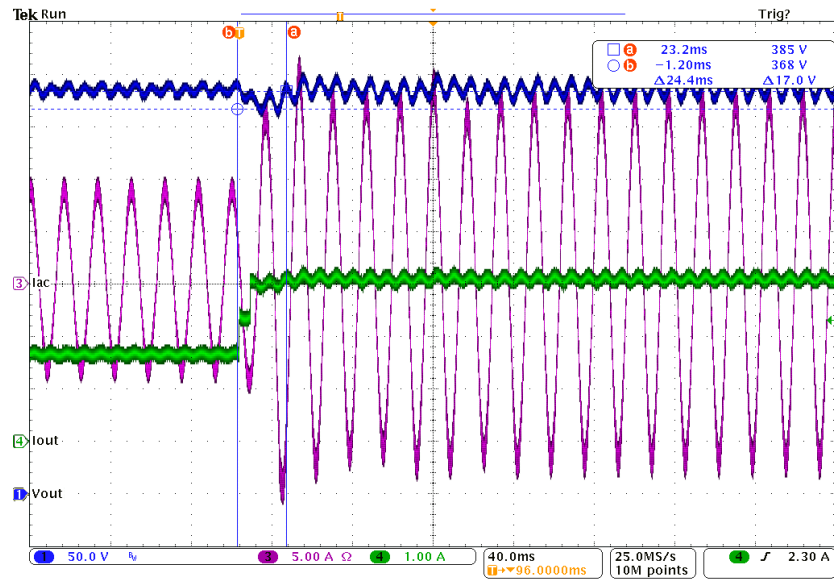
50 to 0% step load

# Step Load Response – Low Line ( 115Vac)



9 Sep 2016  
00:25:30

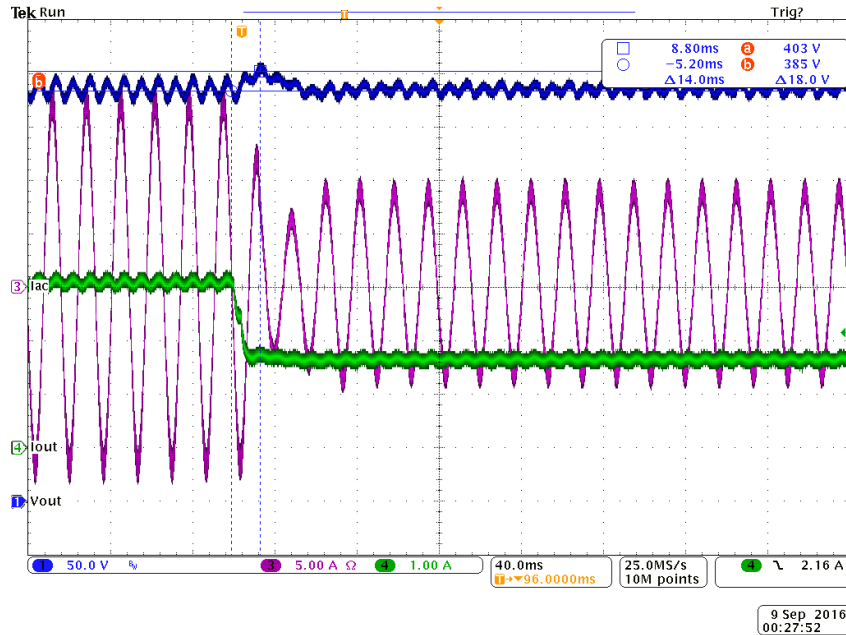
0 to 50% step load



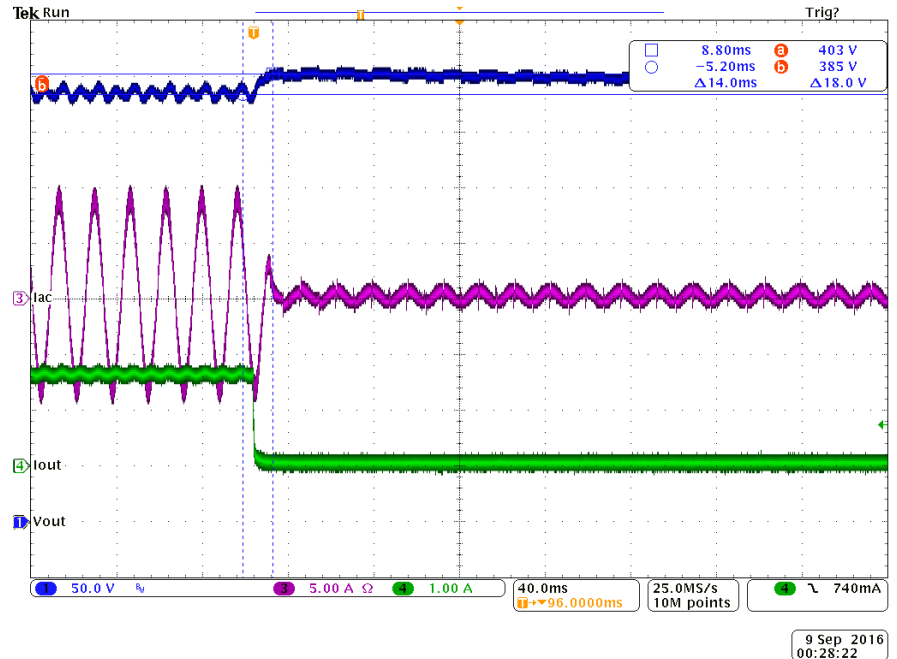
9 Sep 2016  
00:26:56

50 to 100% step load

# Step Load Response – Low Line ( 115Vac)

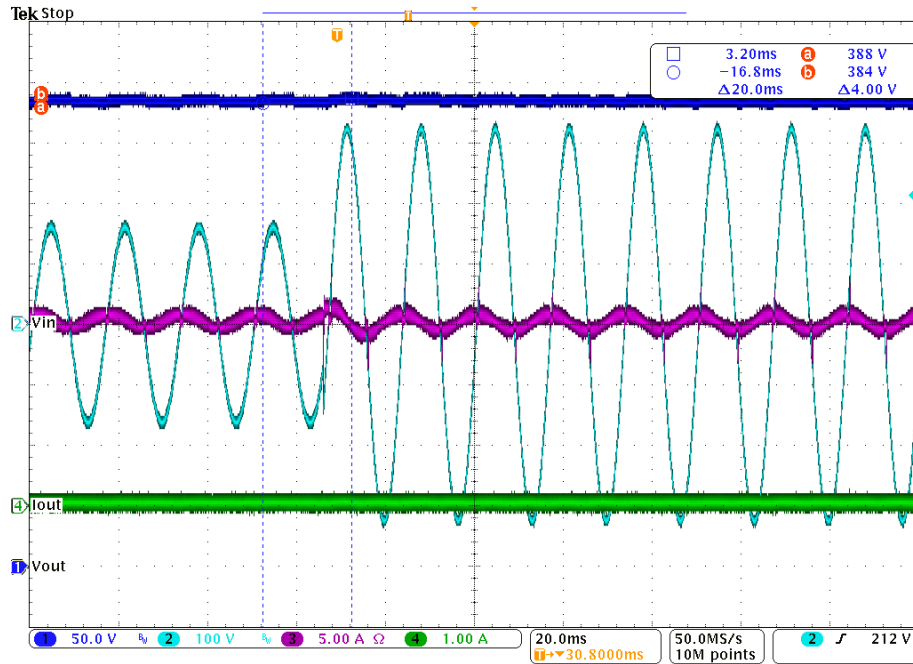


100% to 50% step load

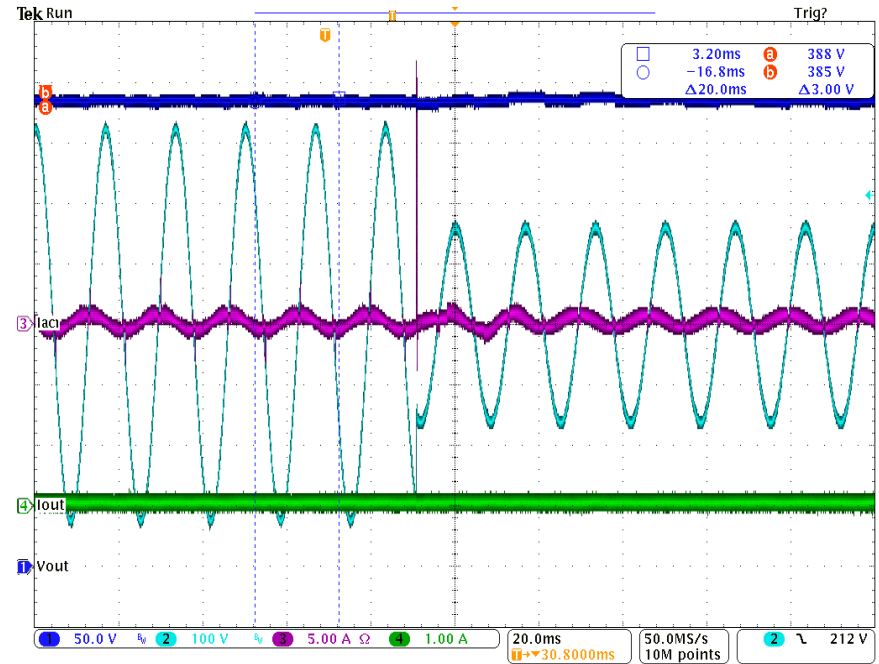


50 to 0% step load

# AC voltage transient Test 115V-230V at 0A load



9 Sep 2016  
03:40:43



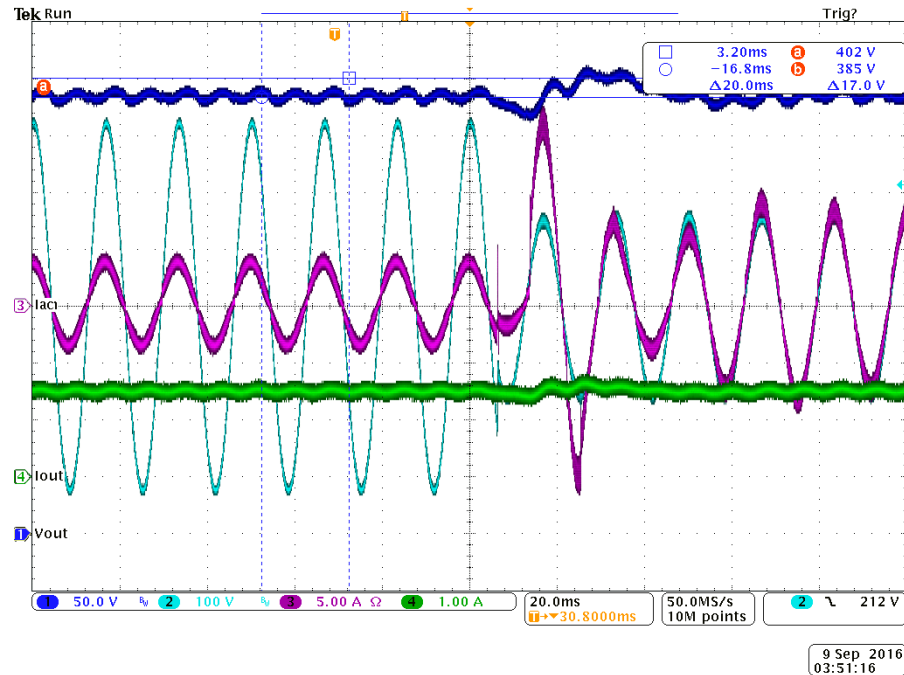
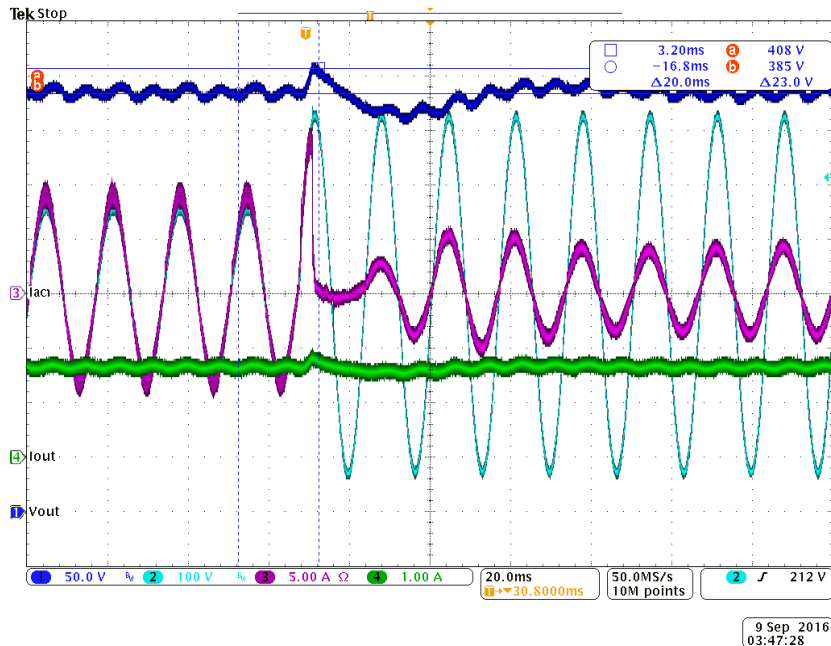
9 Sep 2016  
03:50:19

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# AC voltage transient Test 115V-230V at 1kW load



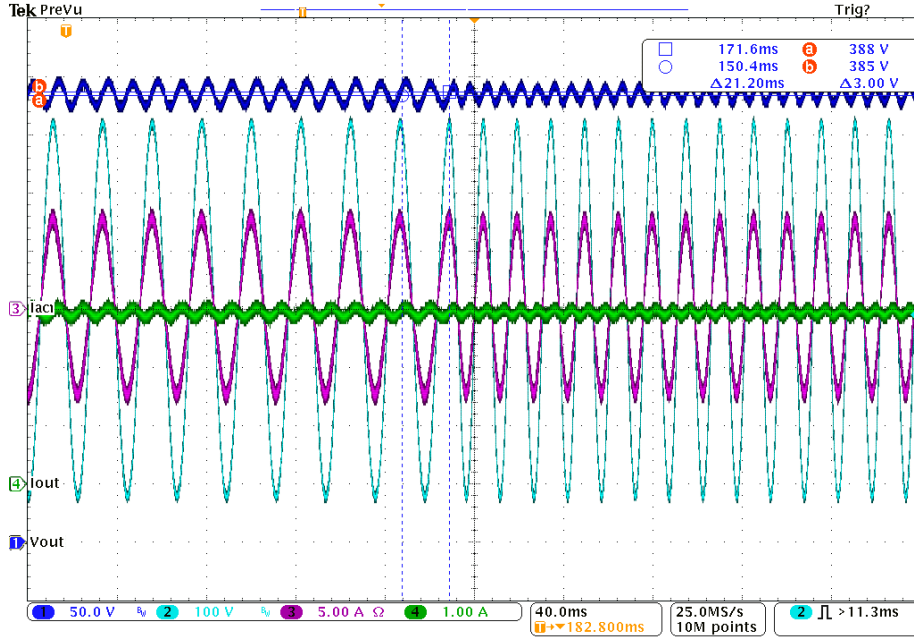
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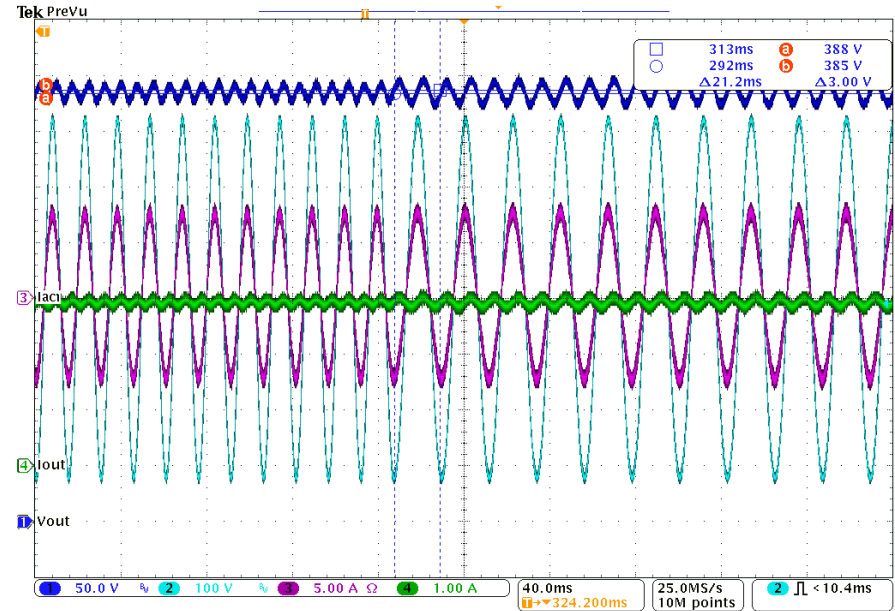




# AC frequency transient Test (45 – 66Hz @ 230V 1kW)



9 Sep 2016  
04:15:09



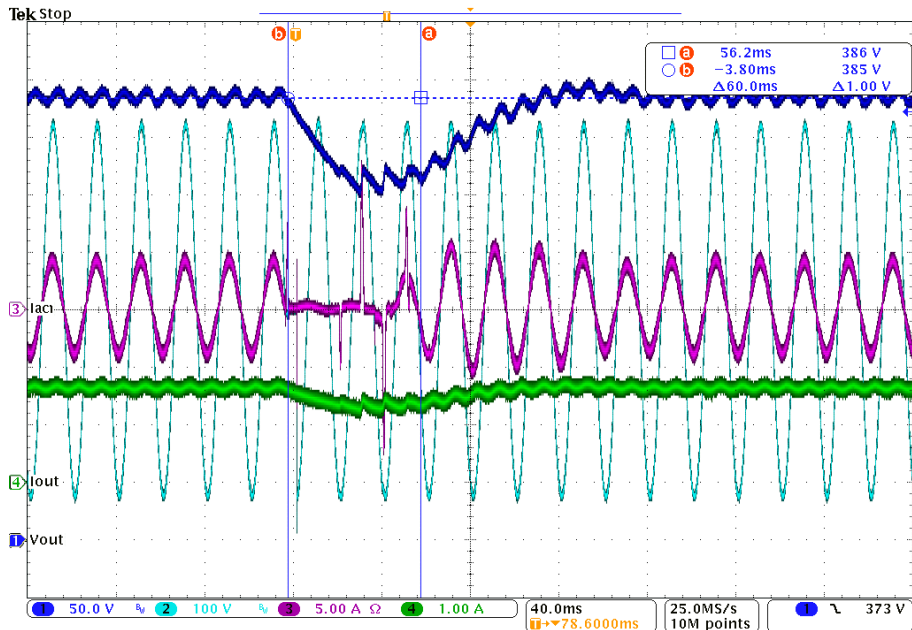
9 Sep 2016  
04:17:30

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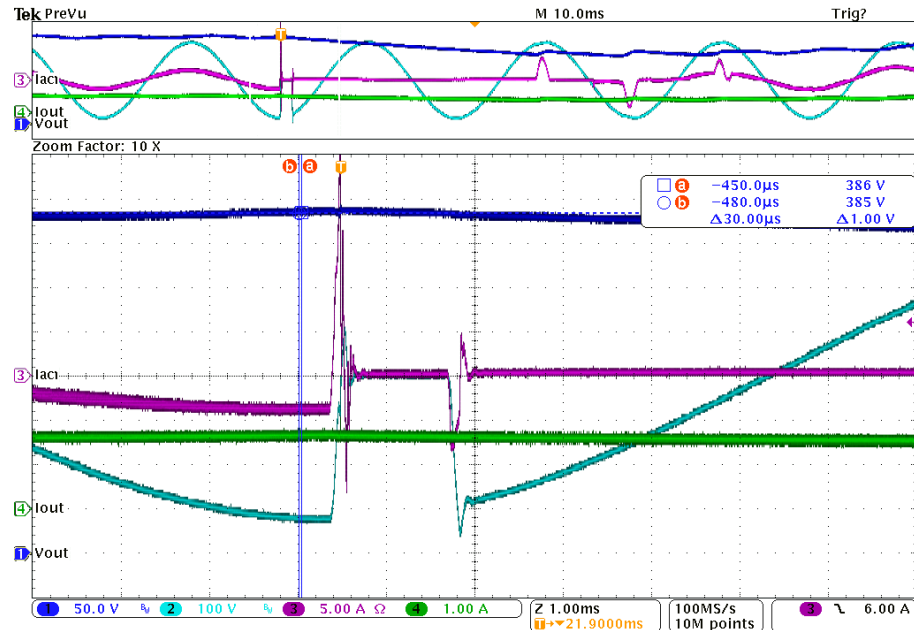
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# AC drop and recovery test



9 Sep 2016  
04:33:48



9 Sep 2016  
04:40:07

Pending issues: Current software disables PFC for three AC cycles when AC drop is detected.

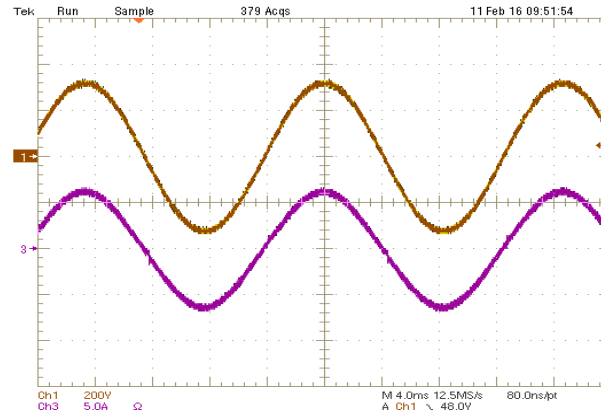
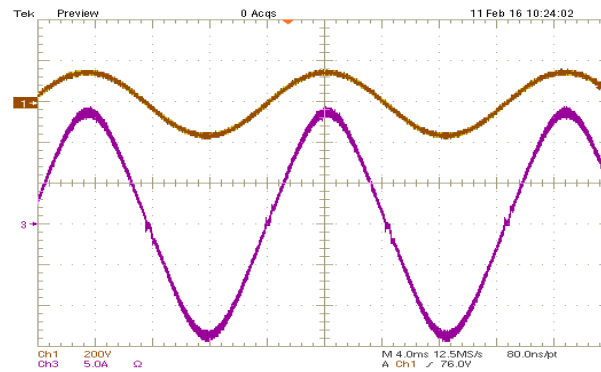
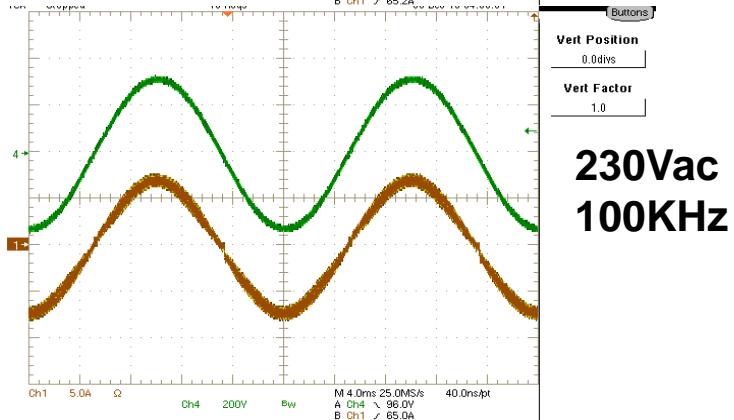
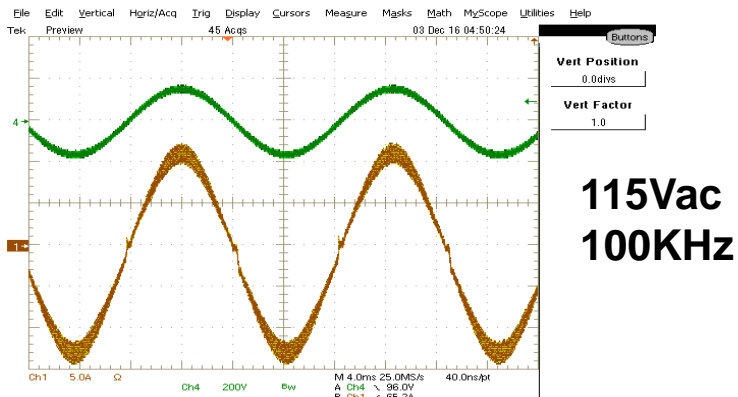
A large reverse current occurs at AC dropping edges.

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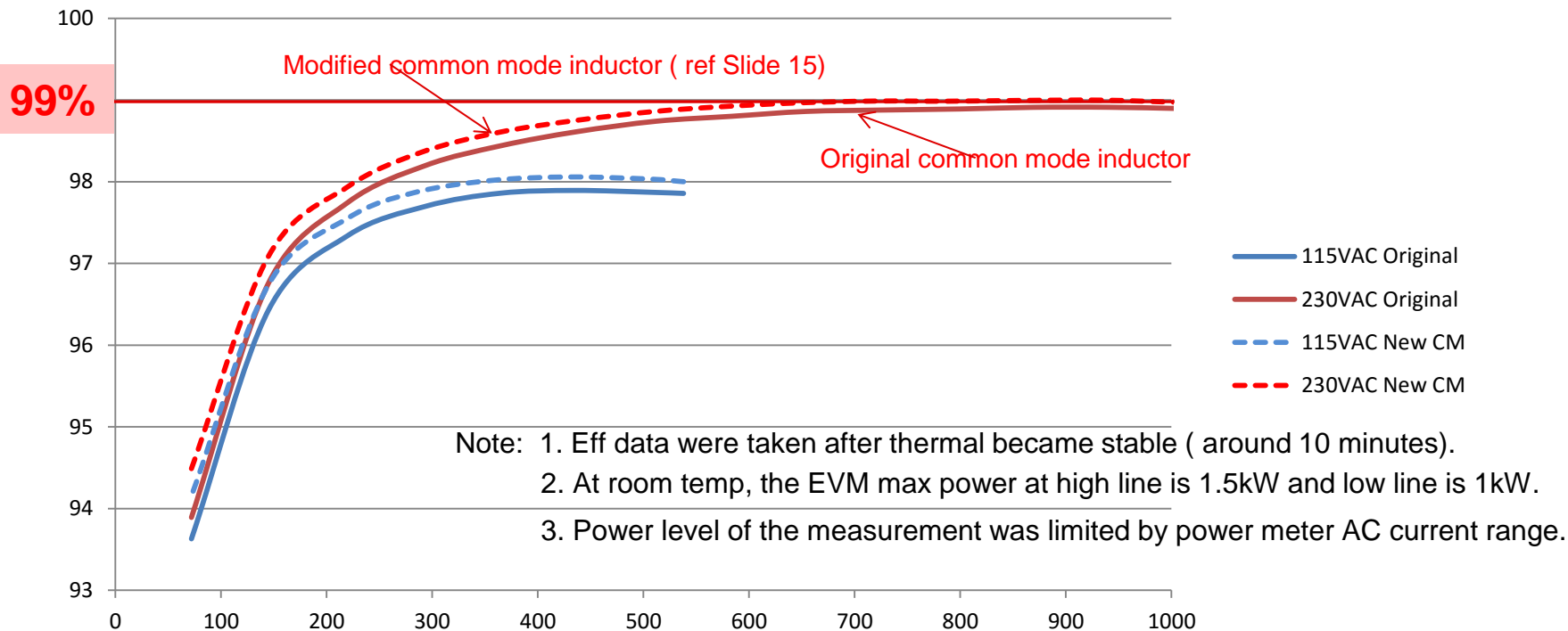
# AC Current Waveforms at Full Load



Redefining the future of power through high voltage – [www.ti.com/highvoltage](http://www.ti.com/highvoltage)

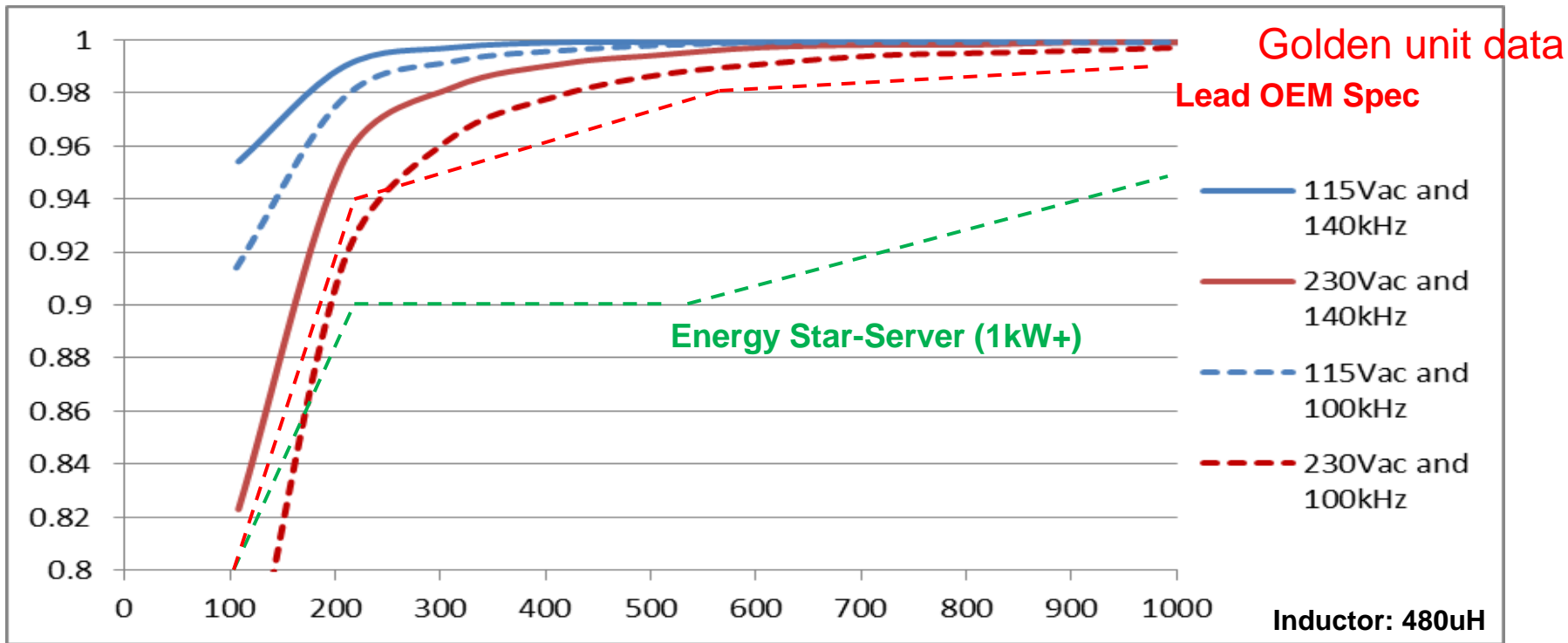
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# Efficiency



Note: Bias loss not included

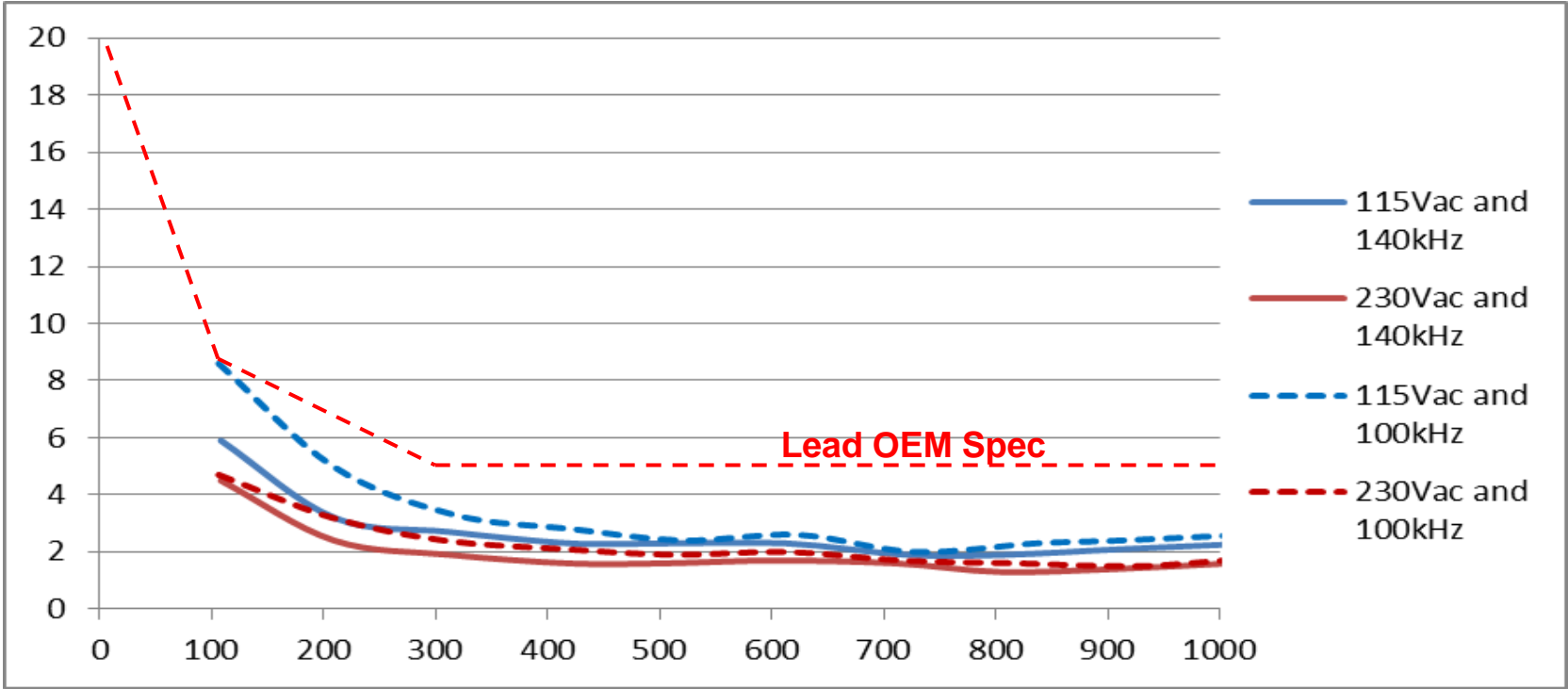
# Improving Power Factor by Using Higher fs



Higher switching frequency increases current loop bandwidth and improves PF.

# THD

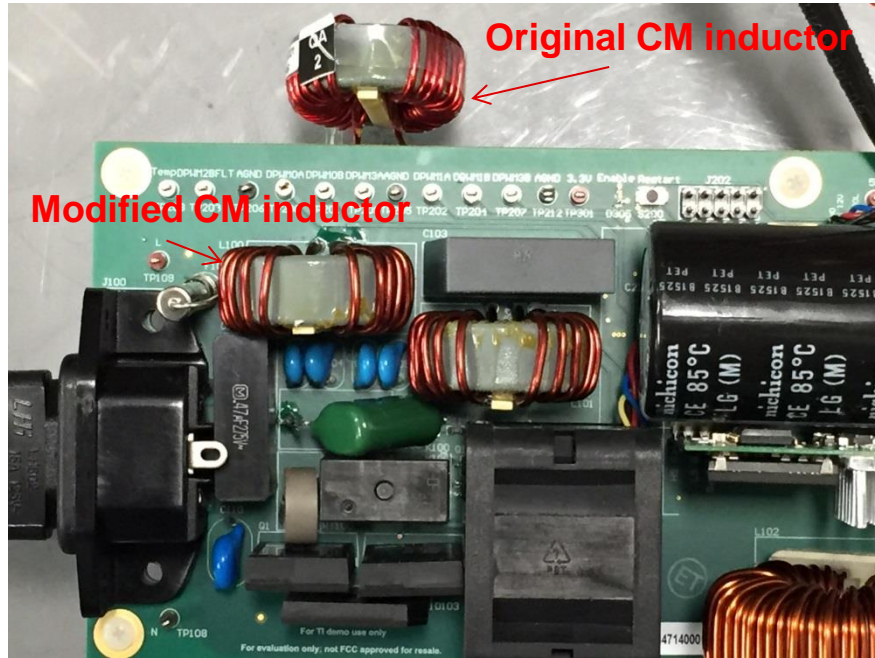
Golden unit data



Inductor: 480uH

# Backup Slides

EMI design modification to improve efficiency



- Change CM inductor from 1.68mH to 1.2mH ( DCR decreased from 30mΩ to 20mΩ by using 16 AWG 10 turn instead of 18 AWG wire)
- C103 changed from 1uF to 0.47uF
- C107 changed from 1uF to 2.2uF

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